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FORM HDP-1449 (Based on Form PTO-1449)

PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION

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Sheet 1 of 2

3/21/	28/
FILING DATE	GROUP
Aram, Farbod	
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MP0426	10/8/4534
ATTORNEY DOCKET No.	SERIAL NO.

U.S. P	ATENT DO	CUMENTS				
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	H. c.	4,535,233	08/13/1985	Abraham		
2.	H- e.	4,564,818	01/14/1986	Jones		
3.	H- = .	4,724,315	02/09/1988	Goerne		
4.	H_ c.	4,764,732	08/16/1988	Dion		
5.	M- e.	4,772,859	09/20/1988	Sakai	_	
6.	H. c.	4,914,402	04/03/1990	Dermitzakis et al.		
7.	H. C.	5,010,588	04/23/1991	Gimlett		
8.	4.c,	5,345,073	09/06/1994	Chang et al.		
9.	H. c.	5,382,920	01/17/1995	Jung	_	
10.	H.c.	5,532,471	07/02/1996	Khorramabadi et al.	_	
11.	H. c.	5,646,573	07/08/1997	Bayruns et al.	_	
12.	H. c.	6,037,841	03/14/2000	Tanji et al.	_	
13.	H-c,	6,057,738	05/02/2000	Ku et al.		
14.	4.c.	6,084,478	07/04/2000	Mayampurath	_	
15.	И- с,	6,114,913	09/05/2000	Entrikin	<u> </u>	
16.	U-<,	6,122,131	09/19/2000	Jeppson		

FORE	IGN PATEN	T DOCUMENTS	i				
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translatio Yes	n No
1.	И-с,	H6-61752	03/04/1994	Japan (Computer Translation)		х	

Examiner: Henry choe Date Considered: 9/27/25	

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Sheet 2 of 2

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FORE	IGN PATEN	T DOCUMENTS					
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translatio Yes	n No
2.	H- C.	H6-61752	03/04/1994	Japan			X
3.	H_ =.	JP 406061752A	03/04/1994	Japan		Х	

OTHE	R DOCUME	NTS (including Author, Title, Date, Pertinent Pages, etc.)
Ref. Desig.	Examiner's Initials	
1.	H. c.	Charles A. Holt, "Electronic Circuits Digital and Analog", by John Wiley & Sons, Inc., 1978, pp. 423, 431, and 436.
2.	H.c.	U.S. Serial No. 10/072,843, filed February 2, 2002, Entitled, "Nested Transimpedance Amplifier", pp. 1-17, and 7 sheets of drawings.
3.	И.с.	U.S. Serial No. 10/459,731, filed July 11, 2003, entitled, "Nested Transimpedance Amplifier With Capacitive Cancellation of Input Parasitic Capacitance", pp. 1-46 and 19 sheets of drawings.
4.	H.c.	You et al., SA 21.2 "A Multistate Amplifier Topology with Nested Gm-C Compensation for Low-Voltage Application", IEEE International Solid-State Circuits Conference, Digest of Technical Papers, ISSCC97, February 8, 1997, pp. 348-349.

Examiner: Henry Choe Date Considered: 9/27/05

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PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION

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Sheet 1 of 1

ATTORNEY DOCKET No.	SERIAL NO.
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1.	H. c.	2002/0153949	10/24/2002	Yoon			

FORE	IGN PATEN	IT DOCUMENTS	3		·		
Ref. Desig	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translati Yes	ion No
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OTHE	OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)				
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Examiner: Hany chor Date Considered: 9/26/05